

ABSTRACT OF THE DISCLOSURE

In one embodiment, a system for deskewing signals on parallel bus channels includes several parallel channels that carry several data input signals and a pair of complementary voltage and timing reference (VTR) signals. The system also includes several receivers, with
5 each receiver coupled to receive a data input signal and the pair of complementary VTR signals. The output of each receiver is based in part on a comparison of the received data input signal with the pair of complementary VTR signals. In one embodiment, a pair of VTR signals is selected from several pairs of VTR signals to widen the skew band on each channel. In one embodiment, each channel is skewed using a programmable delay circuit having a delay value
10 based on the alignment of a data input signal relative to a pair of complementary VTR signals.

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